

16-18 rewritten so as to eliminate the mutliple dependency. Attached hereto is a marked-up version of the claims as amended. Entry of this preliminary amendment is respecfully requested.

Respectfully submitted,

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**MARKED-UP VERSION OF AMENDED CLAIMS**

**The Claims have been amended as follows:**

16. The semiconductor integrated circuit of Claim 13, [any of Claims 13, 14 and 15]

wherein the width of the plurality of interconnection lines is  $0.18 \mu\text{m}$  or less.

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17. The semiconductor integrated circuit of Claim 13, [any of Claims 13, 14 and 15]

wherein the plurality of interconnection lines are a plurality of address bus lines.

18. The semiconductor integrated circuit of Claim 13, [any of Claims 13, 14 and 15]

wherein signals propagating through the plurality of interconnection lines are digital signals of an image or voice.

The following claims have been added:

35. The semiconductor integrated circuit of Claim 14, wherein the width of the plurality of interconnection lines is  $0.18 \mu\text{m}$  or less.

36. The semiconductor integrated circuit of Claim 15, wherein the width of the plurality of interconnection lines is  $0.18 \mu\text{m}$  or less.

37. The semiconductor integrated circuit of Claim 14, wherein the plurality of interconnection lines are a plurality of address bus lines.

38. The semiconductor integrated circuit of Claim 15, wherein the plurality of

interconnection lines are a plurality of address bus lines.

39. The semiconductor integrated circuit of Claim 14, wherein signals propagating through the plurality of interconnection lines are digital signals of an image or voice.

40. The semiconductor integrated circuit of Claim 15, wherein signals propagating through the plurality of interconnection lines are digital signals of an image or voice.